

Notice of References CitedApplication/Control Number
10/004,386Applicant(s)/Patent Under
Reexamination
PAPASOULIOTIS ET AL.Examiner
Craig A ThompsonArt Unit
2813

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,030,881	02-2000	Papasouliotis et al.	438/424
	B	US-6,027,663	02-2000	Martin et al.	216/66
	C	US-4,361,461	11-1982	Chang, Robert P. H.	204/192.32
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S. "Silicon Processing for the VLSI Era-Volume 1" Lattice Press, Sunset Beach, CA 1987, pages 349-353 and 539-552.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.